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CIRCUIT FOR EXTRACTING VIDEO DATA IN OVERLAY CARD

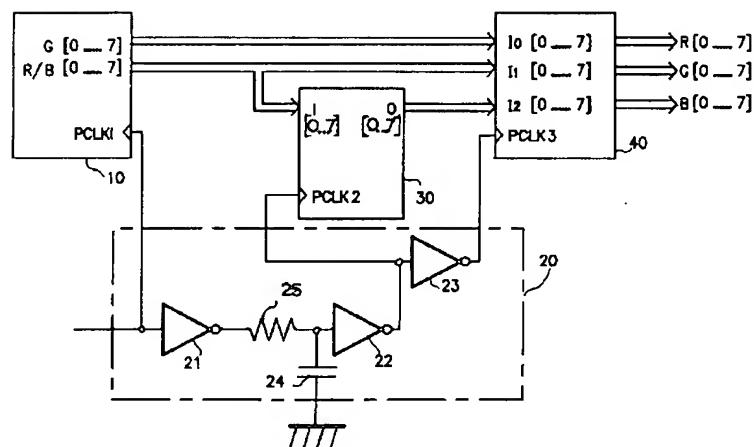
Representative drawing

(57) Abstract:

PURPOSE: A circuit for extracting video data in an overlay card is provided to easily extract 24-bits video data from the 16-bits video data that is multiplexed, and enable the variable clock pulse from the video card to be used without modification in hardware.

CONSTITUTION: A video output unit(10) outputs two of R, G, B signals via one of 8-bits data bus, and outputs one of the R, G, B signals via the other 8-bits data bus. A data latch unit(30) extracts one of the signals multiplexed by the video output unit(10). A digital-to-analog conversion unit(40) converts the 16-bits signal from the video output unit(10) and the 8-bits signal from the data latch unit (30) to analog signals. A clock generation unit(20) supplies a clock signal to the units

(10,30,40). The clock generation unit(20) includes inverters(21,22,23).



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